L Number	Hits	Search Text	DB	Time stamp
	278	((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/10/14 16:25
2	23	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/156.ccls.	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/14 16:25
4	9441	((sram or (static adj6 random adj5 access adj5 memory)) and "memory device") and transistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/14 16:26
5	3	(((sram or (static adj6 random adj5 access adj5 memory)) and "memory device") and transistor) and ("four\$1transistor" or 4T\$1SRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/14 16:26
6	83	(((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor) and ((((sram or (static adj5 random adj5 access adj6 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/\$7.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:27
7	147		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/14 16:27
8	2	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/191.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/14 16:27
9	23	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/156.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/14
10	51	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/154.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/10/14 16:28
11	2	(((sram or (static adj6 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/191.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/10/14 16:28
-	47057	sram or (static adj5 random adj5 access adj5 memory)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/01 01:46
-	19882	(sram or (static adj5 random adj5 access adj5 memory)) and transistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/01 01:46
_	25 _. 8	((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/14 16:25

-	21	(((sram or (static adj5 random adj5	USPAT;	2004/10/14
		access adj5 memory)) and transistor) and	US-PGPUB;	16:25
		("four-transistor" or 4T-SRAM)) and	EPO; JPO;	
		365/156.ccls.	DERWENT;	
	1,500	/ ///	IBM_TDB	2004/10/14
_	16318	(sram or (static adj5 random adj5 access	USPAT;	2004/10/14
	İ	adj5 memory)) and "memory device"	US-PGPUB;	16:26
			EPO; JPO;	
			DERWENT; IBM TDB	
	0656	//	USPAT;	2004/10/14
-	8656		USPAT; US-PGPUB;	16:26
		adj5 memory)) and "memory device") and	1	16:26
		transistor	EPO; JPO; DERWENT;	
			IBM TDB	
i _	3	(((sram or (static adj5 random adj5	USPAT;	2004/10/14
-	3	(((Staim of (Static adjs fandom adjs access adj5 memory)) and "memory device")	US-PGPUB;	16:26
		and transistor) and ("four\$1transistor"	EPO; JPO;	10.20
		or 4T\$1SRAM)	DERWENT;	
		OI 41515RAM)	IBM TDB	
_	3	(((sram or (static adj5 random adj5	USPAT;	2004/06/01
-	,	access adj5 memory)) and "memory device")	US-PGPUB;	01:54
		and transistor) and 4T\$1SRAM	EPO; JPO;	
		and orangeout, and myronen	DERWENT;	
			IBM TDB	
_	75	(((sram or (static adj5 random adj5	USPAT;	2004/10/14
,	1 ,3	access adj5 memory)) and "memory device")	US-PGPUB;	16:27
		and transistor) and ((((sram or (static	EPO; JPO;	
		adj5 random adj5 access adj5 memory)) and	DERWENT;	
		transistor) and ("four-transistor" or	IBM TDB	
	1	4T-SRAM)) and 365/\$7.ccls.)		
_	0	1	USPAT;	2004/06/01
		Silva	US-PGPUB;	01:55
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	29	chou-chung-\$.in.	USPAT;	2004/06/01
			US-PGPUB;	01:55
	-		EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	2	chung-chou.in.	USPAT;	2004/06/01
	1	-	US-PGPUB;	01:55
	İ		EPO; JPO;	
	ĺ		DERWENT;	
	[IBM_TDB	*
-	0	chung-chou-\$.in.	USPAT;	2004/06/01
			US-PGPUB;	01:55
	1		EPO; JPO;	
	1		DERWENT;	
	_	l , _ , _ ,	IBM_TDB	2004/05/05
-	0	chou-chung-c.in.	USPAT;	2004/06/01
			US-PGPUB;	01:55
ļ			EPO; JPO;	
			DERWENT;	
1.	1 10	Wfound1tmangiates and Ame1chan	IBM_TDB	2004/06/01
-	12	"four\$1transistor" or 4T\$1SRAM	USPAT;	2004/06/01
			US-PGPUB;	01:57
			EPO; JPO; DERWENT;	
			IBM TDB	
l _	137	(((sram or (static adj5 random adj5	USPAT;	2004/10/14
	13/	access adj5 memory)) and transistor) and	US-PGPUB;	16:27
		("four-transistor" or 4T-SRAM)) and	EPO; JPO;	10.21
		365/\$7.ccls.	DERWENT;	
		300, 41, 6013.	IBM TDB	,
l _	2	(((sram or (static adj5 random adj5	USPAT;	2004/10/14
	2	access adj5 memory)) and transistor) and	US-PGPUB;	16:27
!	1	("four-transistor" or 4T-SRAM)) and	EPO; JPO;	10.27
j :		365/191.ccls.	DERWENT;	
			IBM TDB	
L	L	L		

-	4	(((sram or (static adj5 random adj5 access adj5 memory)) and transistor)	bne	USPAT; US-PGPUB;	2004/10/14 16:27
		("four-transistor" or 4T-SRAM)) and	anu	EPO; JPO;	10.27
		365/226.ccls.		DERWENT; IBM TDB	
-	48	(((sram or (static adj5 random adj5 access adj5 memory)) and transistor) a	and	USPĀT; US-PGPUB;	2004/06/01 01:58
		("four-transistor" or 4T-SRAM)) and 365/154.ccls.		EPO; JPO; DERWENT;	
				IBM TDB	